

<< CANopen Stack for Zynq on BORA <<

Since the beginning of 2015 company emtas has offered projects showing how to integrate the company's CANopen protocol stack into the Zynq based CPU module on BORA by DAVE Embedded Systems (www.dave.eu). The company headquartered in Italy, has been in the embedded modules business since 1998 and has established itself as one of the leading European Companies for embedded solutions.

BORA is the top-class Dual Cortex-A9 + FPGA CPU module by DAVE Embedded Systems, based on the recent Xilinx "Zynq" XC7Z010 / XC7Z020 application processor.

CANopen is the internationally standardized (EN 50325-4) CAN-based higher-layer protocol for embedded control system. It is not only the communication but also application, device, and interface profiles. CANopen provides very flexible configuration capabilities. BORA is an ideal platform for CANopen master and CANopen slave applications.

Download BORA example:

<http://www.emtas.de/download/canopen>

Main features are:

- supports all CANopen services of CiA 301
- ANSIC compatible source code stack
- MISRA-C conform
- Layer Setting Service (LSS) CiA305 included
- extensions for further standards available
- comfortable user interface
- widely configurable and scalable
- 6 months free-of-charge support

The BORA Embedded Linux Kit – BELK extends the BORA module with peripherals and connectors. After unboxing it can be immediately used to evaluate the provided CANopen demo. The CANopen project implements the CiA 401 profile and allows communication with the Linux kernel.

The emtas GmbH CANopen Protocol Stack is a software library that provides all communication services according to the CANopen communication profile CiA301 V4.2 and is already prepared for the upcoming Version V5.0 CANopen FD.

The stack is developed in ANSI-C and it is MISRA-conform. CANopen-conform devices can be quickly and easily developed or extended with the CANopen Protocol Stack.

